



LCD INTF -- LCD Interface Section

- D[0-23] represent data pins on the 8080 (parallel) bus
- The MAX7301 is an IO expander acting as the data bus middleman between the brain and the ssd1963
- The lcd control pins will still be controlled directly from the brain to alleviate timing issues and because its just going to be easier to debug
- The MAX7301 is going to be run at something around 20MHz so we should be ok with timing

