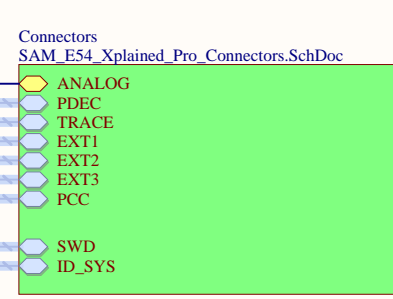
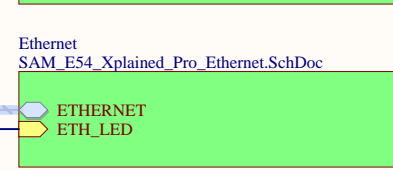
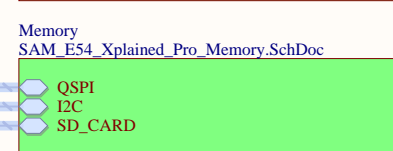
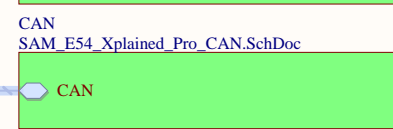
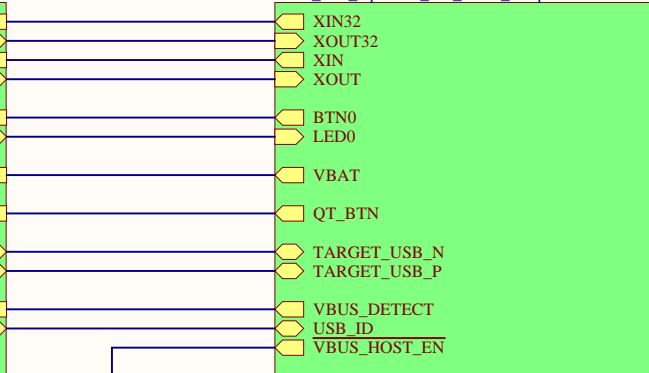
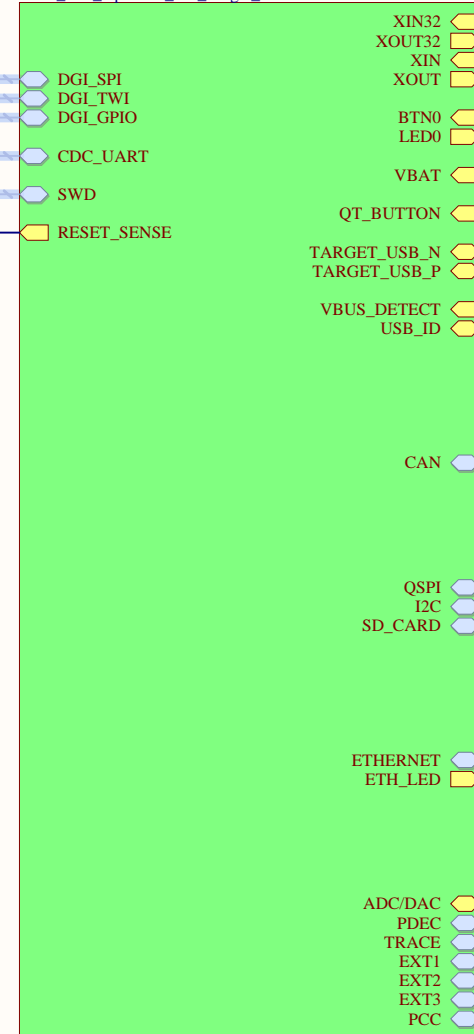
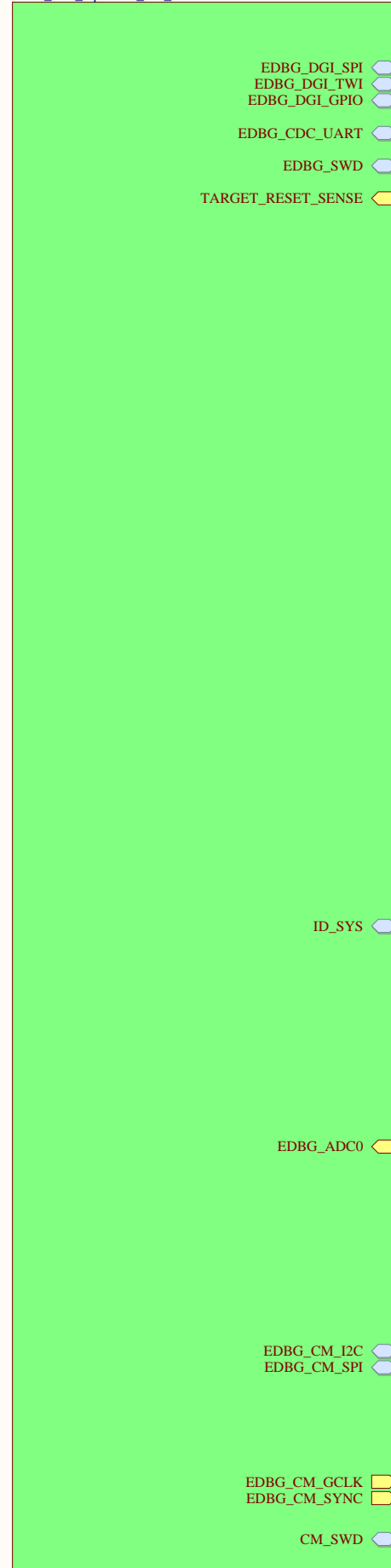


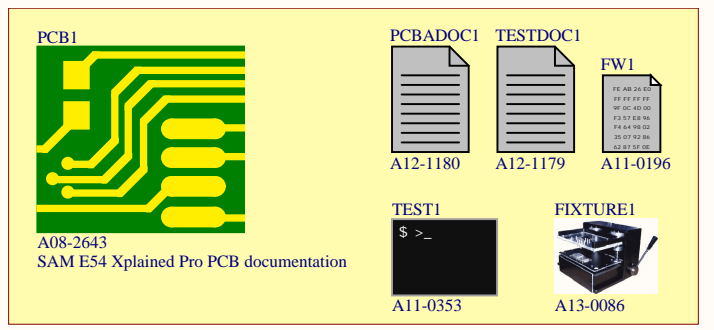
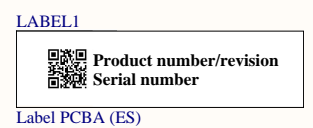
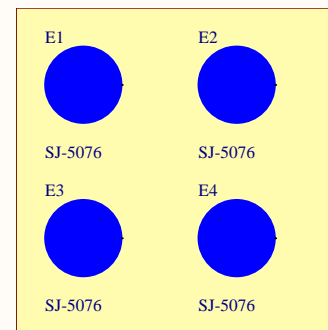
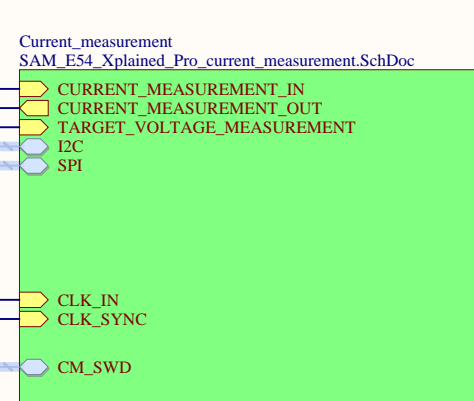
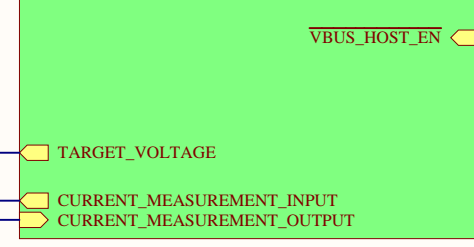
EDBG  
SAM\_E54\_Xplained\_Pro\_EDBG.SchDoc

Target\_MCU  
SAM\_E54\_Xplained\_Pro\_Target\_MCU.SchDoc

MCU Peripherals  
SAM\_E54\_Xplained\_Pro\_MCU\_Peripherals.SchDoc



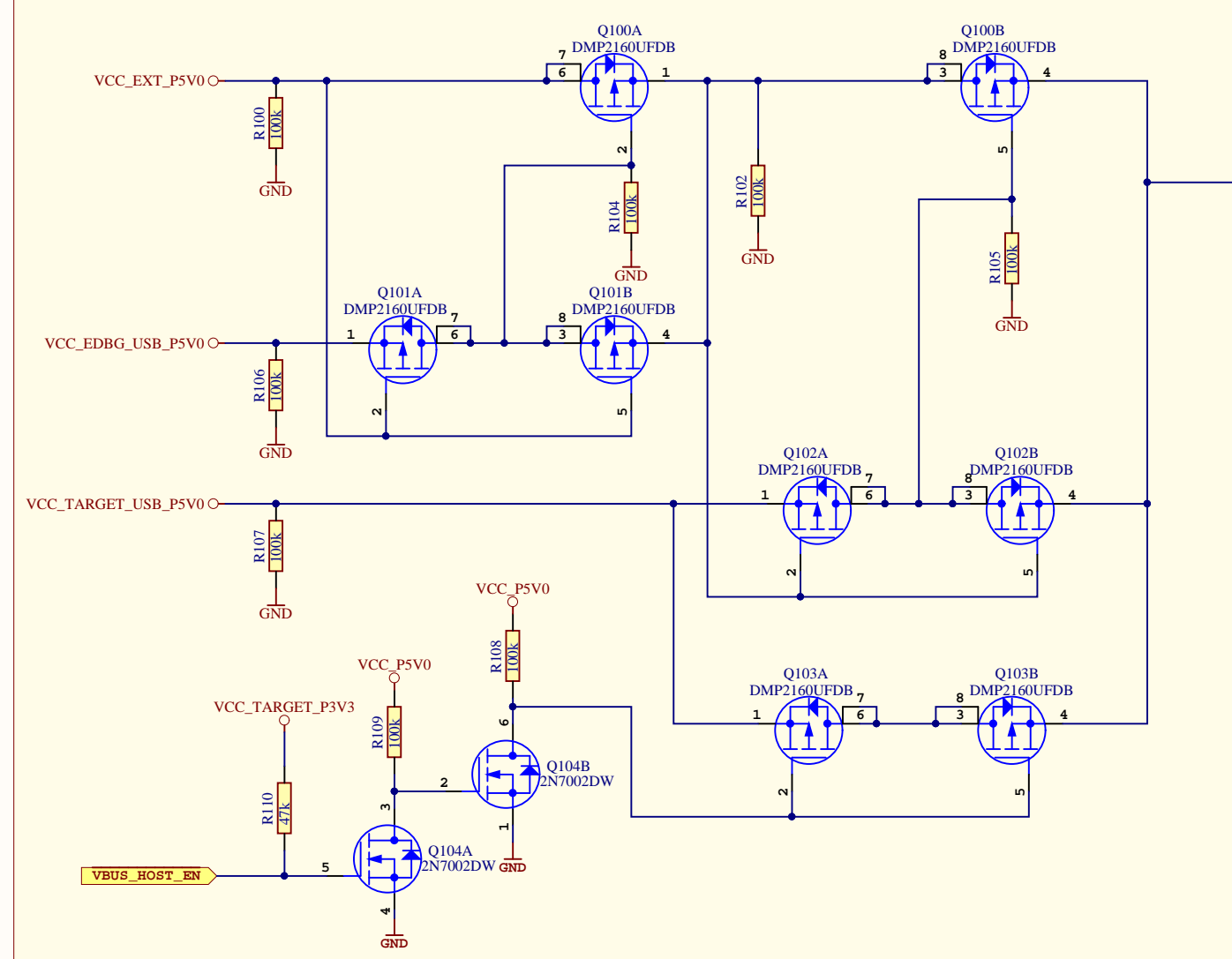
Power\_supply  
SAM\_E54\_Xplained\_Pro\_triple\_input\_power\_supply\_HSW.SchDoc



ATMEL Norway	*			
Vestre Rosten 79	*			
N-7075 TILLER	*			
NORWAY				
Date:	18.01.2017	13:20:55	PAGE: 1 of 13	
Document number:	A09-2748		Revision:	4
TITLE: Top Level Schematics				
SAM_E54_Xplained_Pro_TopLevel.SchDoc				



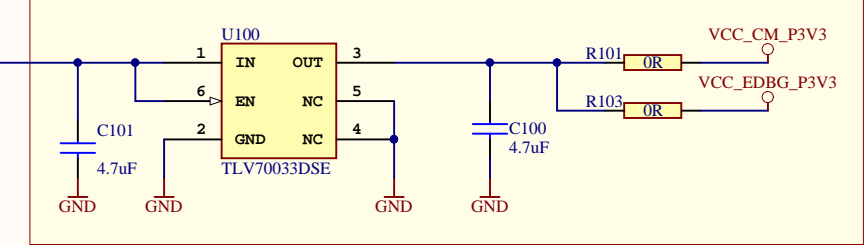
**Power multiplexer**



▲ Power input multiplexer priority:  
 1. VCC\_IN  
 2. VCC\_EDBG\_USB  
 3. VCC\_TARGET\_USB

▲ Pulling #VBUS\_HOST\_EN low will enable power from the board to the USB connector through Q103. #VBUS\_HOST\_EN is automatically controlled by the ID pin in the USB cable, the control signal can also be overridden by setting PC19 low.

**3.3V linear regulator EDBG and XAM**

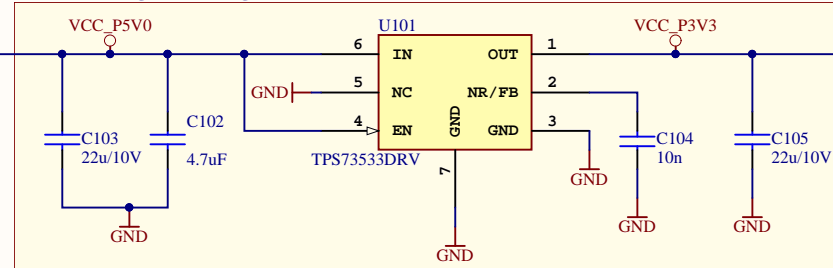


▲ Iout max = 200mA  
 Accuracy 2%  
 Low noise: 48 uVrms (10 Hz to 100 kHz)  
 Dropout 150mV at full load  
 Quiescent current 55 uA (no load)  
 Current limit max 860 mA  
 Thermal shutdown  
 Minimum capacitance required on output is 0.1uF (with less than 200mOhm ESR)

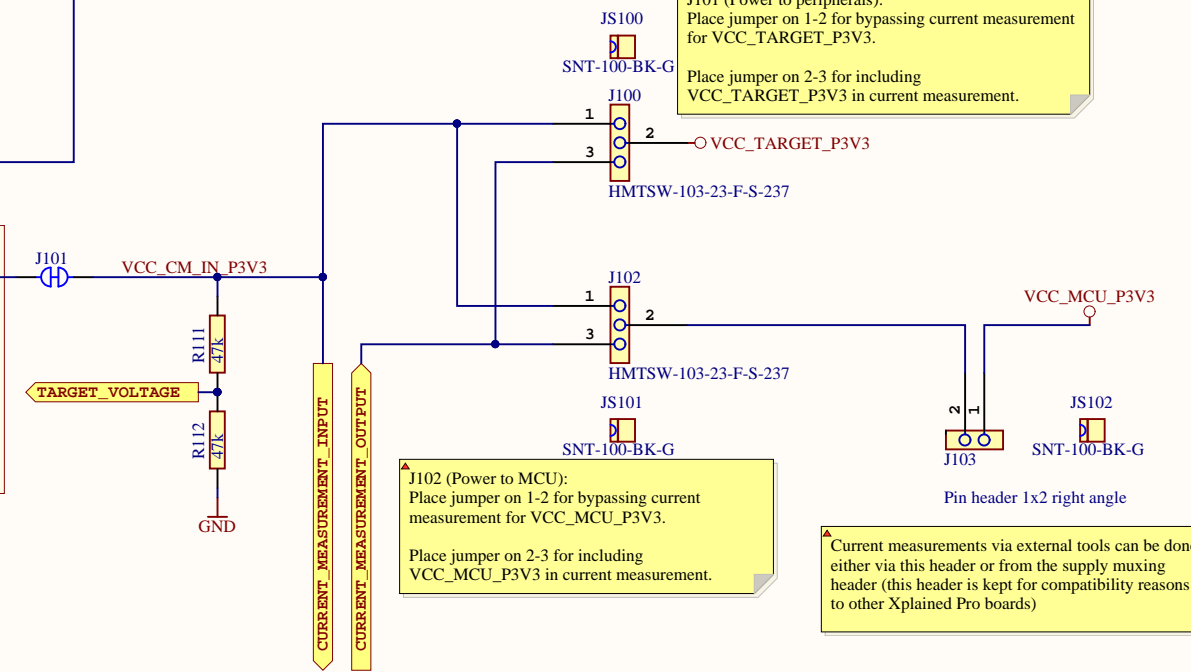
▲ The target peripheral and the MCU can be powered either directly from the regulator or from the current measurement circuitry (XAM). When powered from the current measurement the supply voltage will vary from 3.3 V to 3.2 V due to voltage drop over the current measurement shunt resistor.

▲ J101 (Power to peripherals): Place jumper on 1-2 for bypassing current measurement for VCC\_TARGET\_P3V3. Place jumper on 2-3 for including VCC\_TARGET\_P3V3 in current measurement.

**3.3V linear regulator Target**




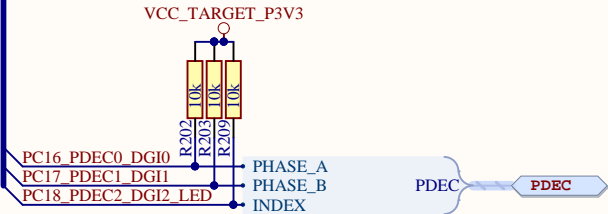
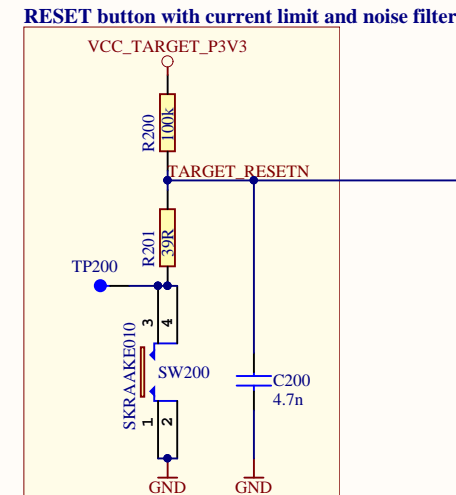
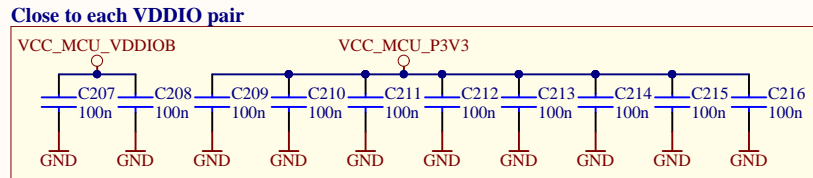
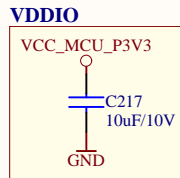
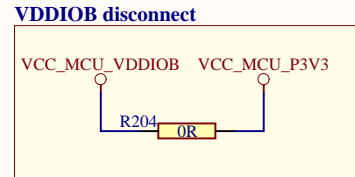
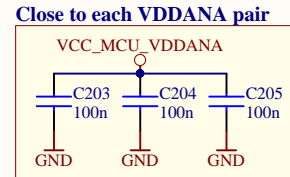
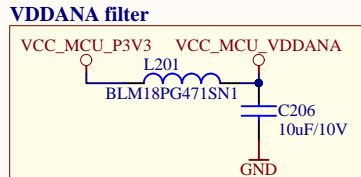
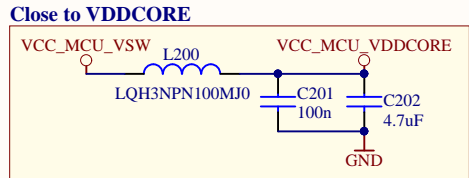
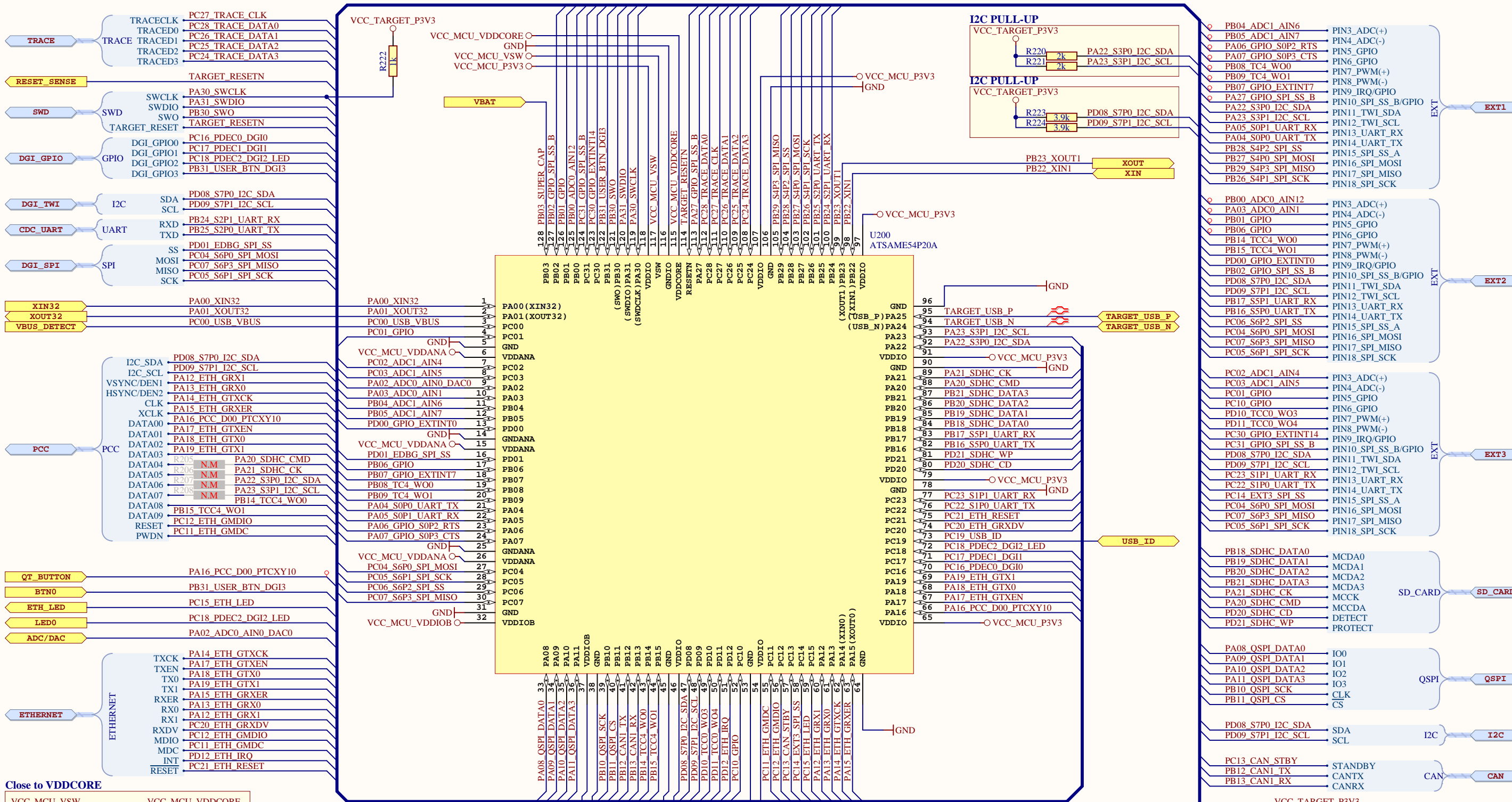
▲ 500mA low noise LDO voltage regulator  
 Noise: 28uVrms  
 Accuracy 2%  
 Dropout 280 mV at full load  
 Quiescent current 46 uA  
 Current limit 1170 mA  
 Thermal shutdown



▲ J102 (Power to MCU): Place jumper on 1-2 for bypassing current measurement for VCC\_MCU\_P3V3. Place jumper on 2-3 for including VCC\_MCU\_P3V3 in current measurement.

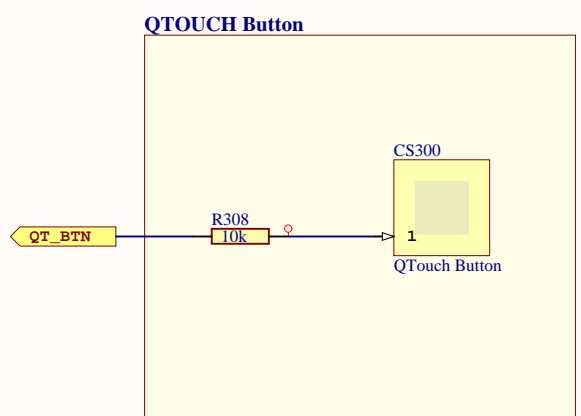
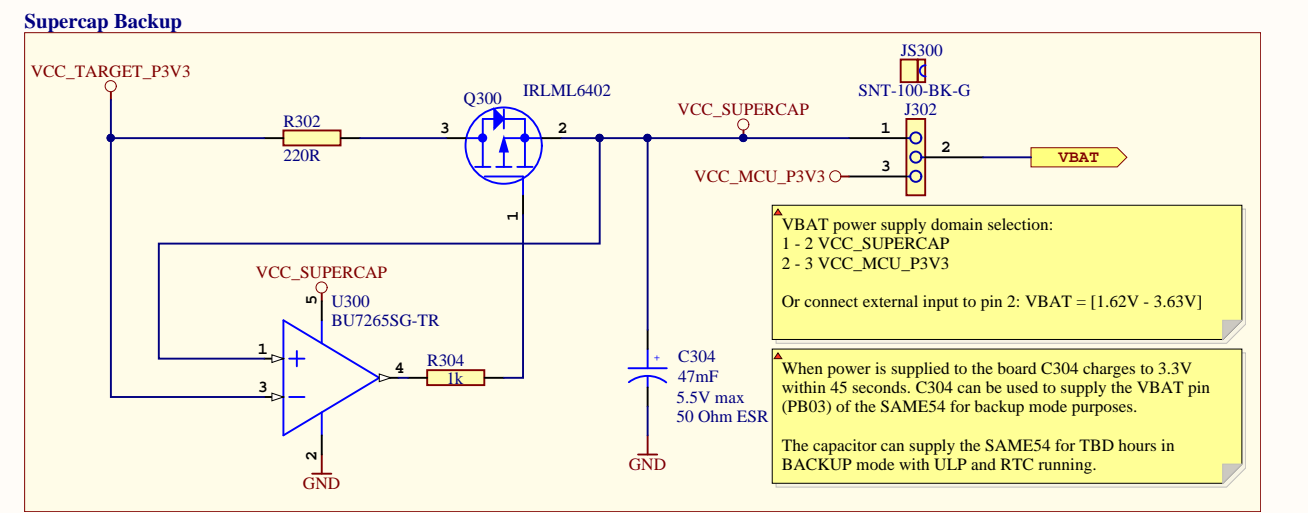
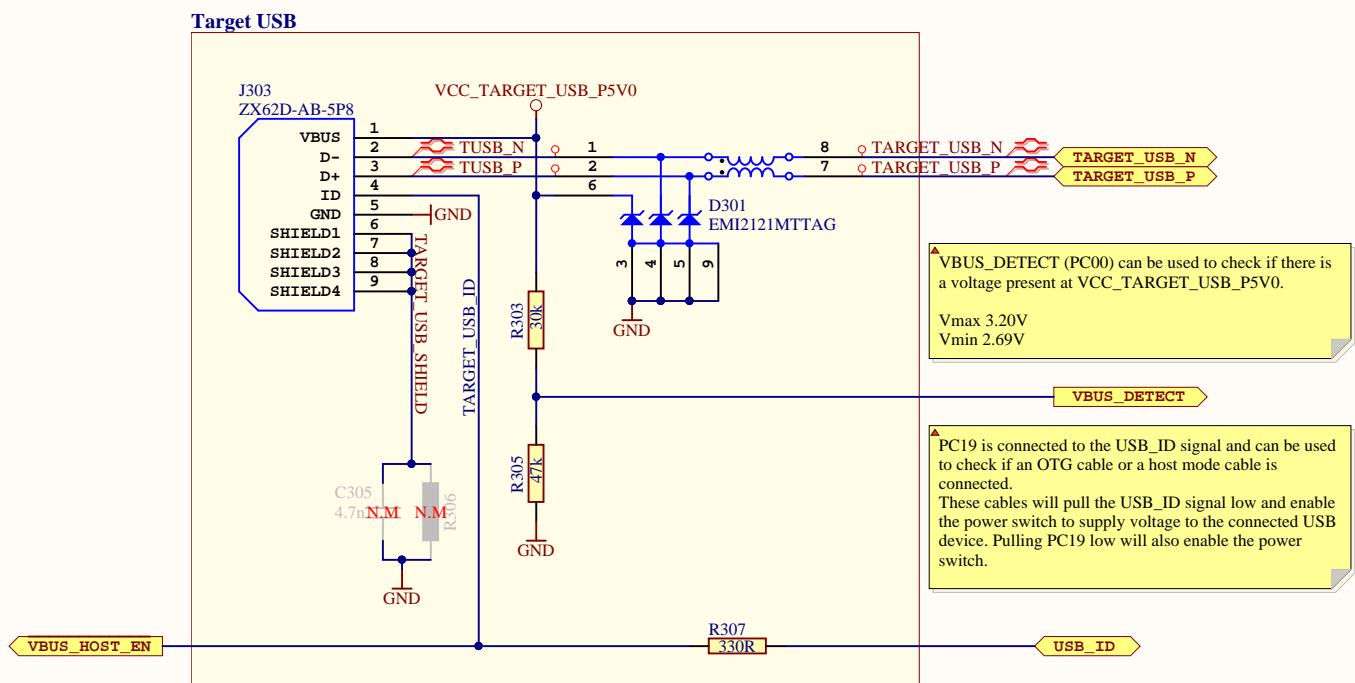
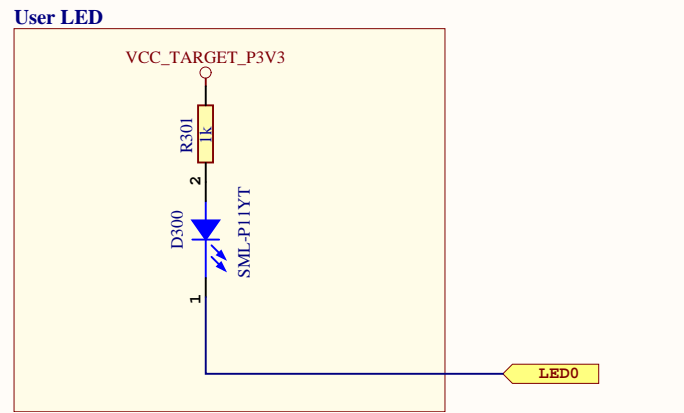
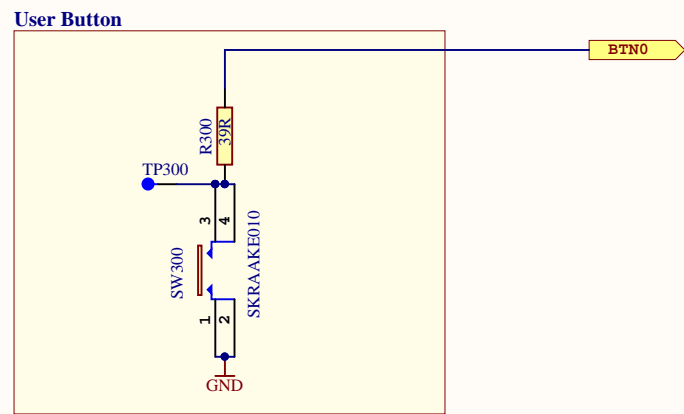
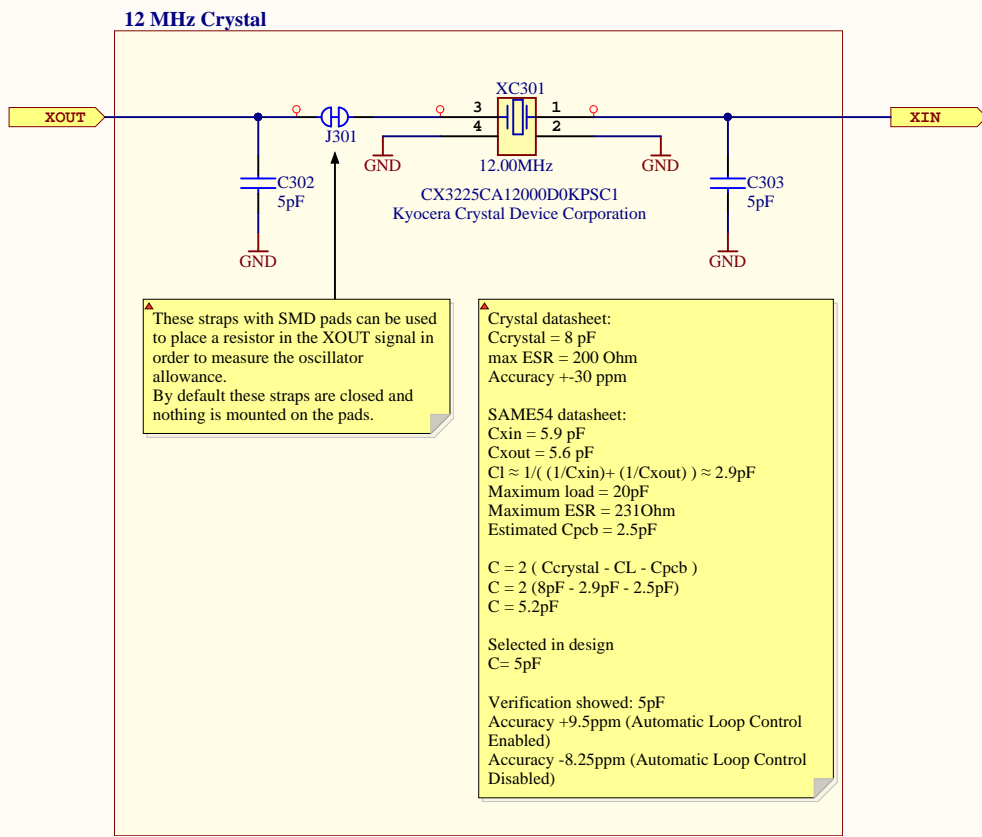
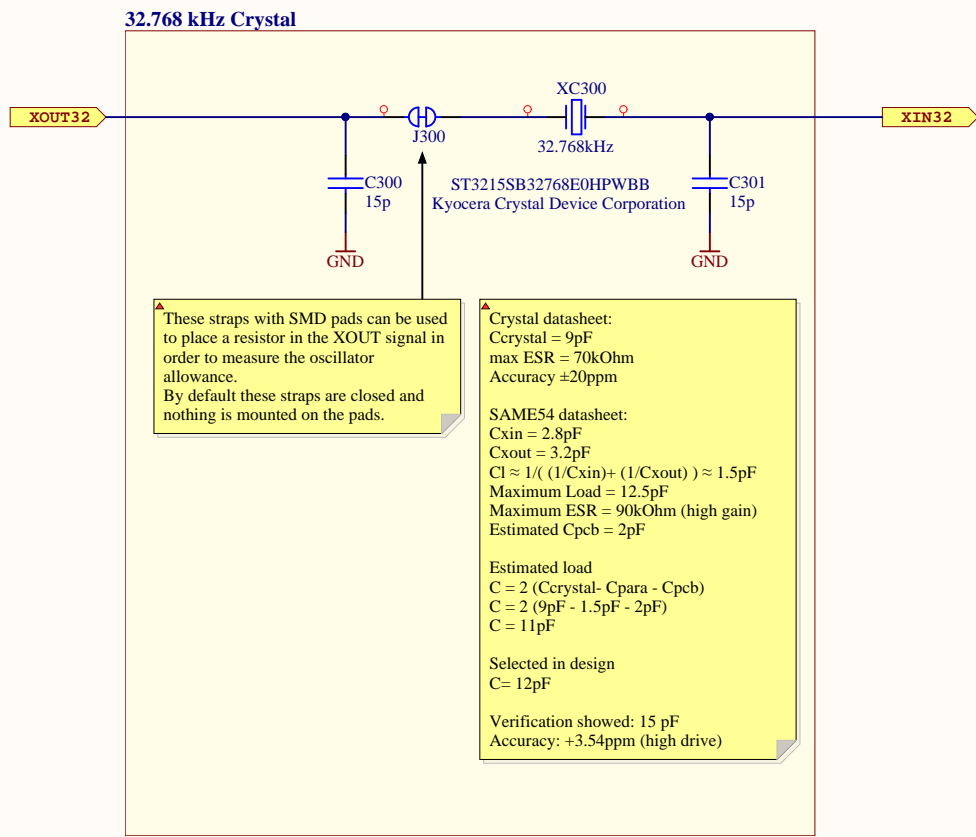
▲ Current measurements via external tools can be done either via this header or from the supply muxing header (this header is kept for compatibility reasons to other Xplained Pro boards)

ATMEL Norway	*			
Vestre Rosten 79	*			
N-7075 TILLER	*			
NORWAY				
Date:	18.01.2017	13:20:55	PAGE: 2 of 13	
Document number:	A09-2748		Revision:	4
TITLE: Power supply				
SAM_E54_Xplained_Pro_triple_input_power_supply_HSW.SchDoc				



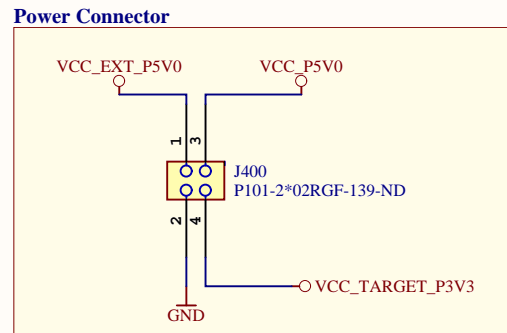
ATMEL Norway	*			
Vestre Rosten 79	*			
N-7075 TILLER	*			
NORWAY				
Date:	18.01.2017	13:20:55	PAGE: 3 of 13	
Document number:	A09-2748		Revision: 4	
TITLE: Target MCU				
SAM_E54_Xplained_Pro_Target_MCU.SchDoc				





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Vestre Rosten 79	*			
N-7075 TILLER	*			
NORWAY				
Date:	18.01.2017	13:20:56	PAGE: 4 of 13	
Document number:	A09-2748		Revision: 4	
TITLE: Target MCU Peripherals				
SAM_E54_Xplained_Pro_MCU_Peripherals.SchDoc				

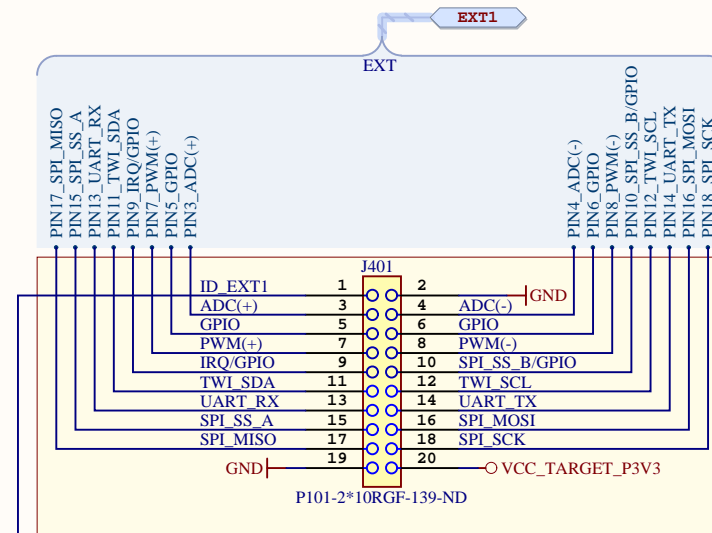




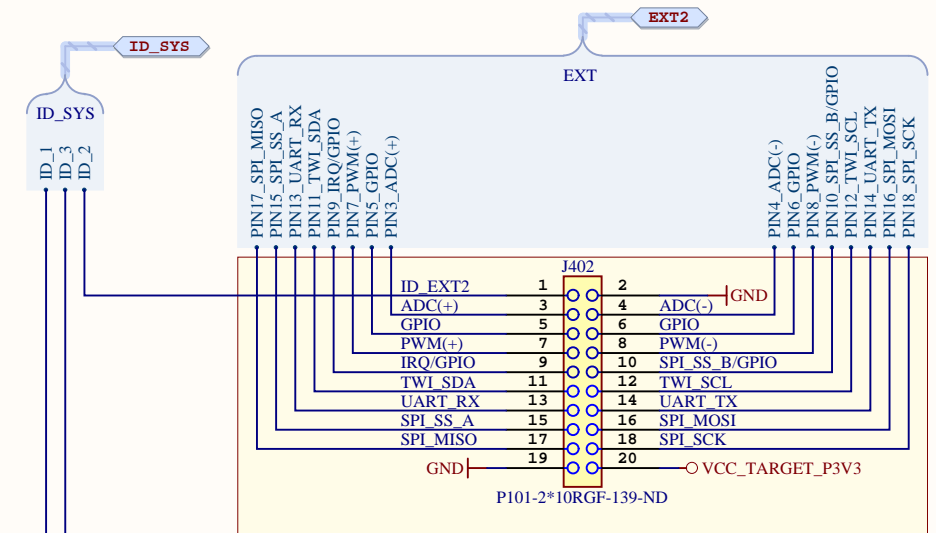
Power inputs/outputs to the Xplained PRO:  
**VCC\_EXT\_P5V0 (input)**  
 This power input can be used to power the whole board and it has a higher priority than the USB power inputs.

**VCC\_P5V0 (output)**  
 This pin is supplied from either VCC\_EXT\_P5V0, VCC\_EDBG\_USB\_P5V0, or VCC\_TARGET\_USB\_P5V0 based on the availability and priority of these supplies.

**VCC\_TARGET\_P3V3 (output)**  
 Target supply voltage (target MCU and peripherals)

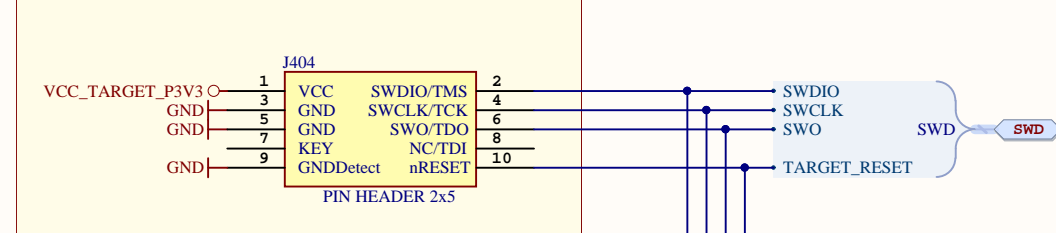


EXT1 extension header

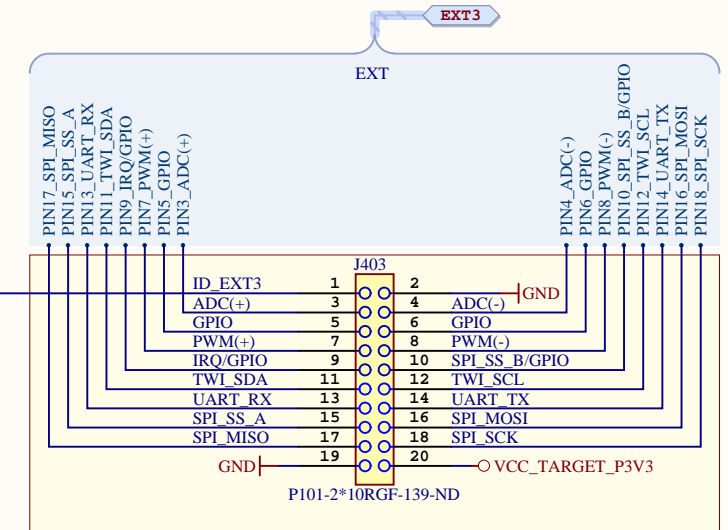
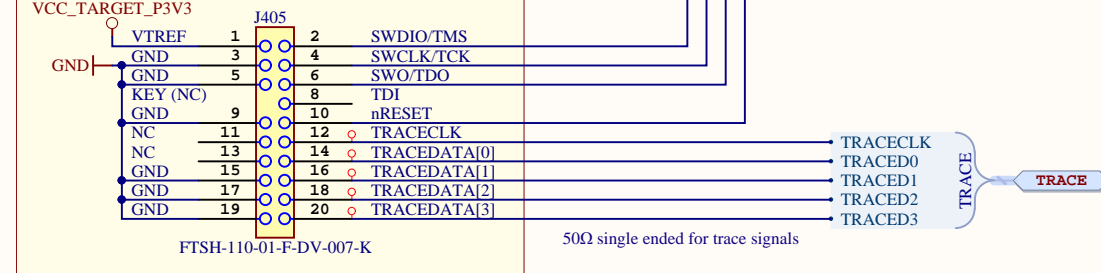


EXT2 extension header

### Cortex Debug Connector for connecting an external programmer/debugger

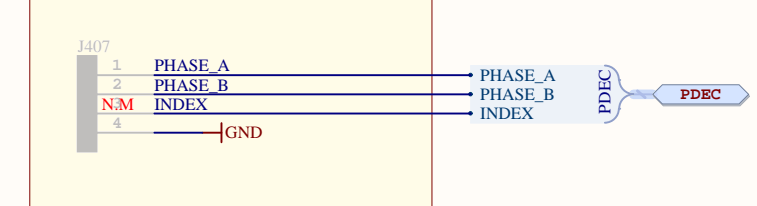


### TRACE (CoreSight 20)

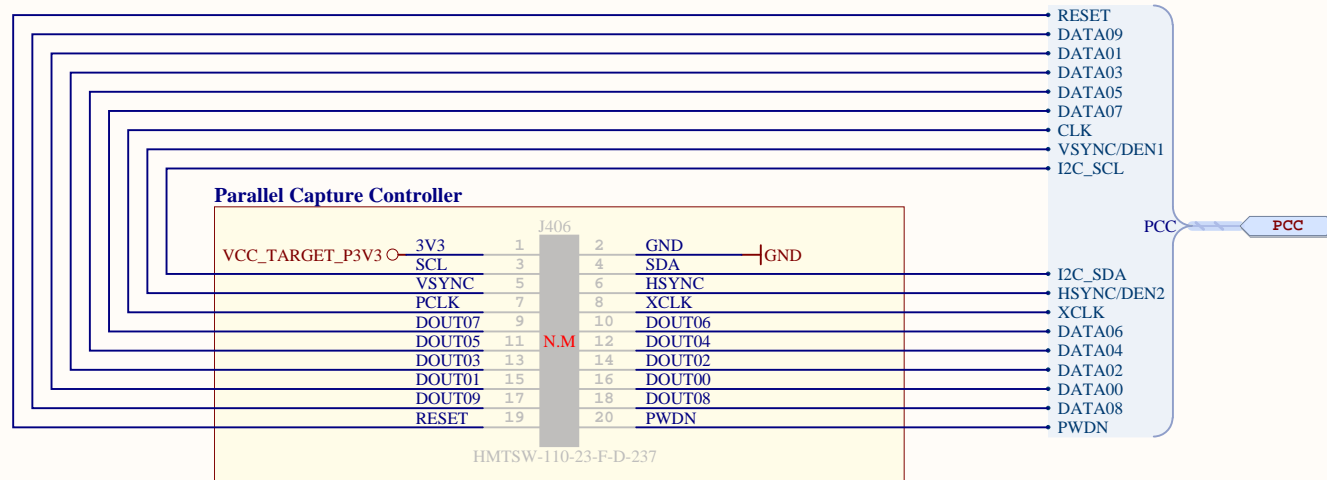
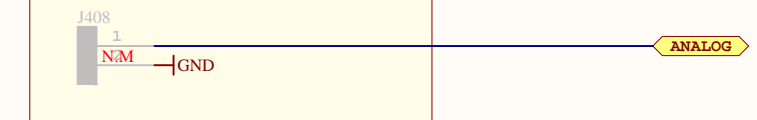


EXT3 extension header

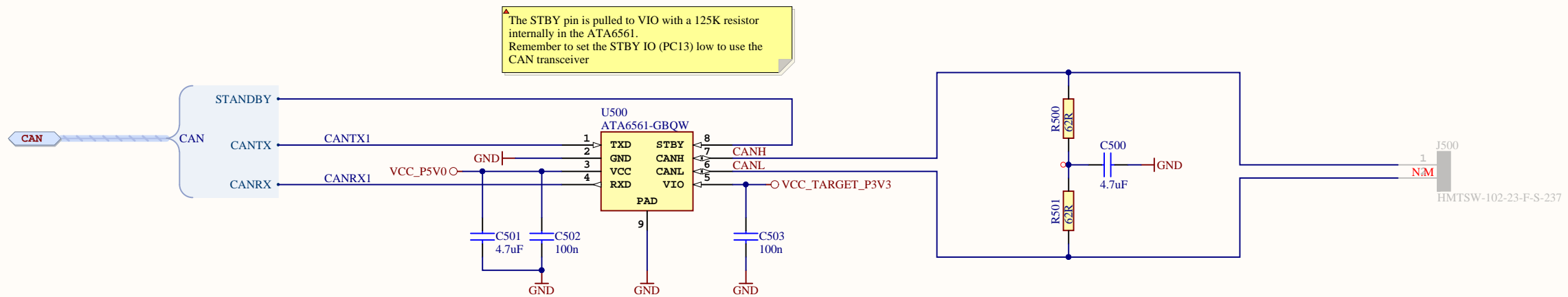
### Position Decoder




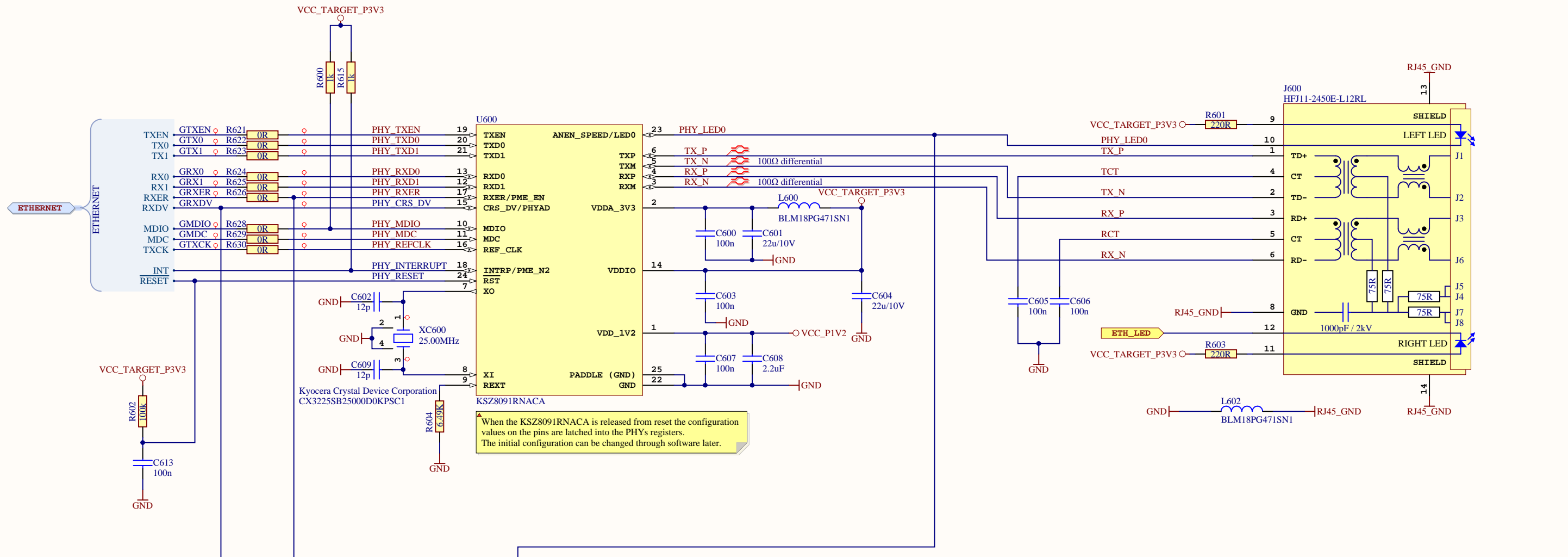
### ADC/DAC Header



ATMEL Norway	*			
Vestre Rosten 79	*			
N-7075 TILLER	*			
NORWAY				
Date:	18.01.2017	13:20:56	PAGE:	5 of 13
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TITLE: Extension connectors				
SAM_E54_Xplained_Pro_Connectors.SchDoc				



ATMEL Norway	*	 Enabling Unlimited Possibilities	
Vestre Rosten 79	*		
N-7075 TILLER	*		
NORWAY			
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Document number:	A09-2748		Revision: 4
TITLE: CAN			
SAM_E54_Xplained_Pro_CAN.SchDoc			

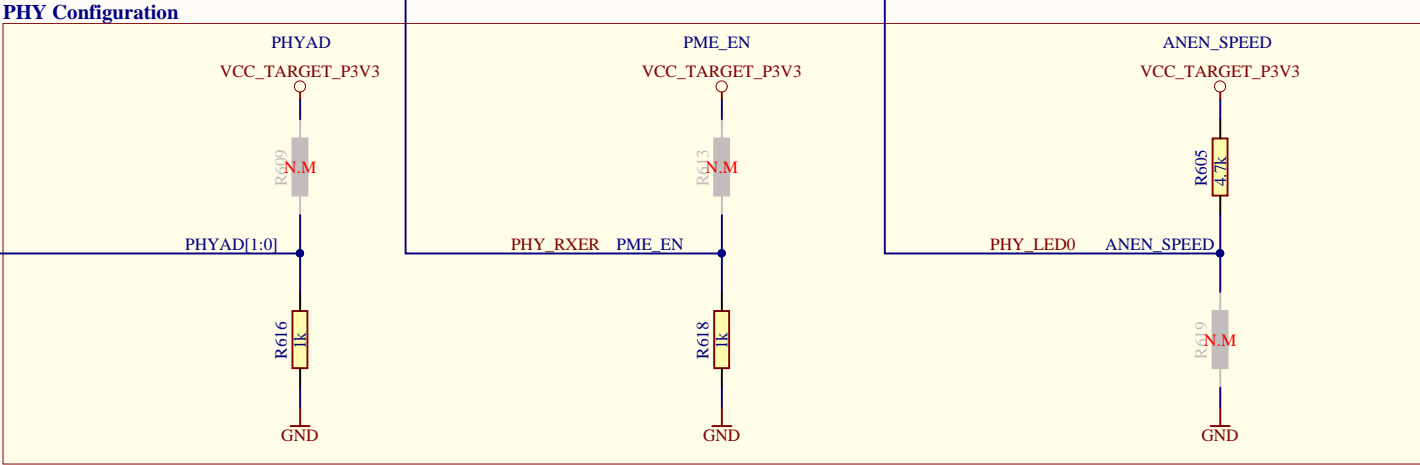


When the KSZ8091RNACA is released from reset the configuration values on the pins are latched into the PHY's registers. The initial configuration can be changed through software later.

PHYAD[1:0] is used to set the PHY's address:  
 Pull-up = 00011b (3h)  
 Pull-down (default) = 00000b (0h)

PME\_EN is used to set PME output for Wake-On-LAN:  
 Pull-up = Enable  
 Pull-down (default) = Disable

ANEN\_SPEED is used to Auto-Negotiation and Speed Mode  
 Pull-up (default) = Enable Auto-Negotiation and set 100Mbps speed  
 Pull-down = Disable Auto-Negotiation and set 10Mbps speed

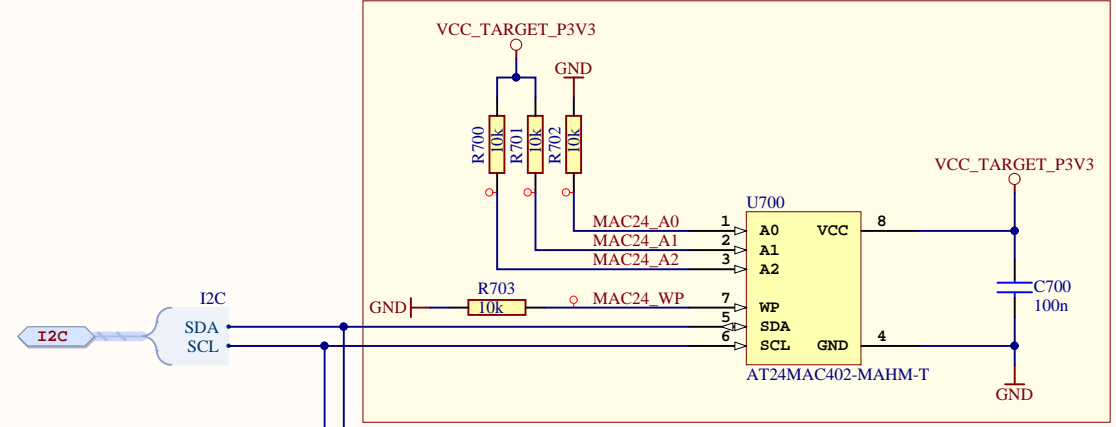


ATMEL Norway	*			
Vestre Rosten 79	*			
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NORWAY				
Date:	18.01.2017	13:20:56	PAGE: 7 of 13	
Document number:	A09-2748		Revision:	4
TITLE: Ethernet				
SAM_E54_Xplained_Pro_Ethernet_SchDoc				

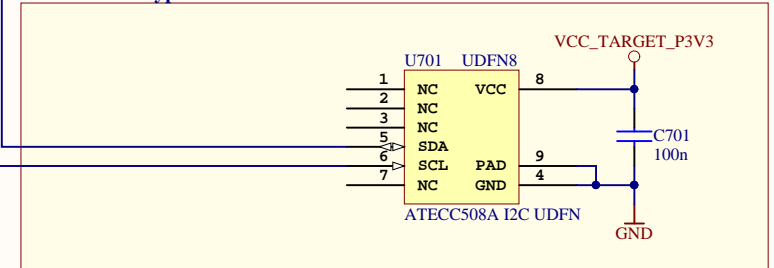
A

A

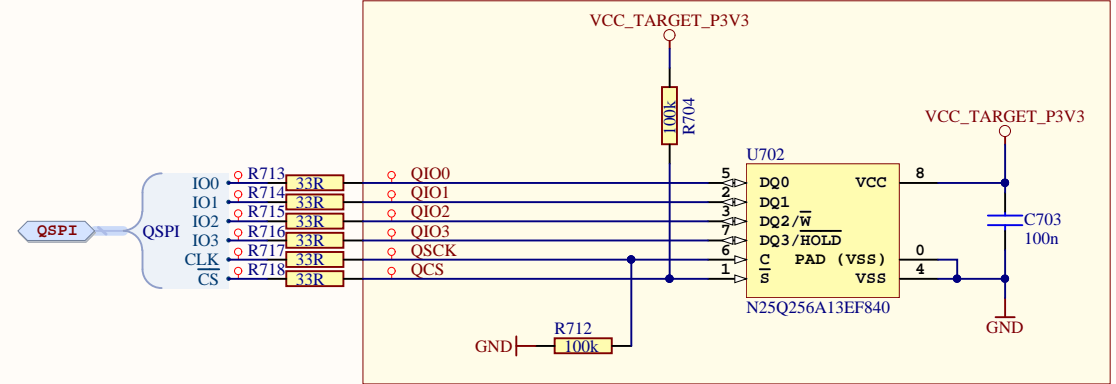
**Serial EEPROM with EIA-48 MAC address**



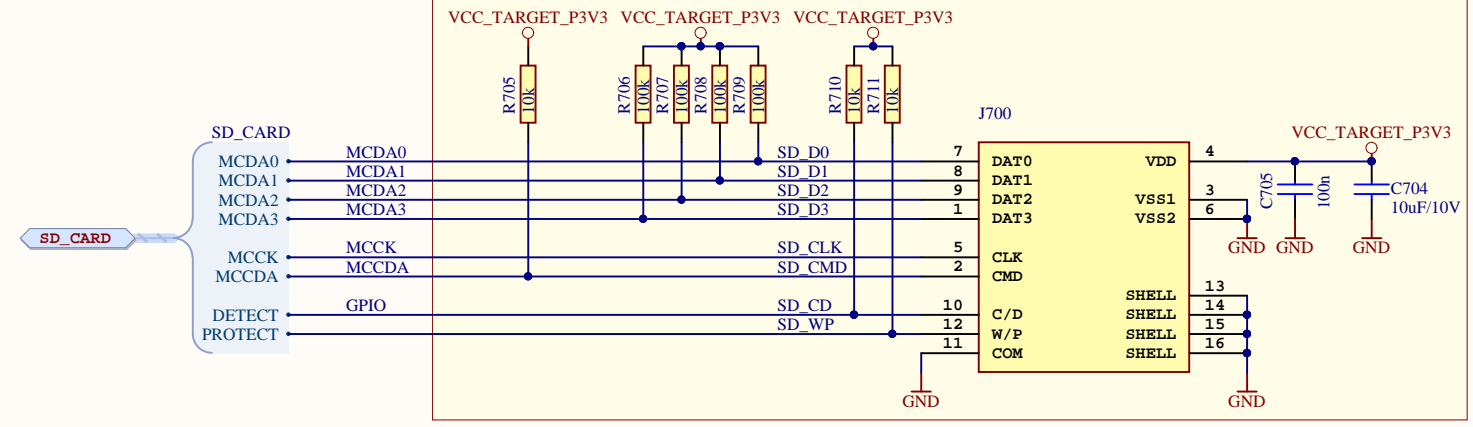
**ATECC508 Crypto**



**QSPI Flash**




**SD-CARD**

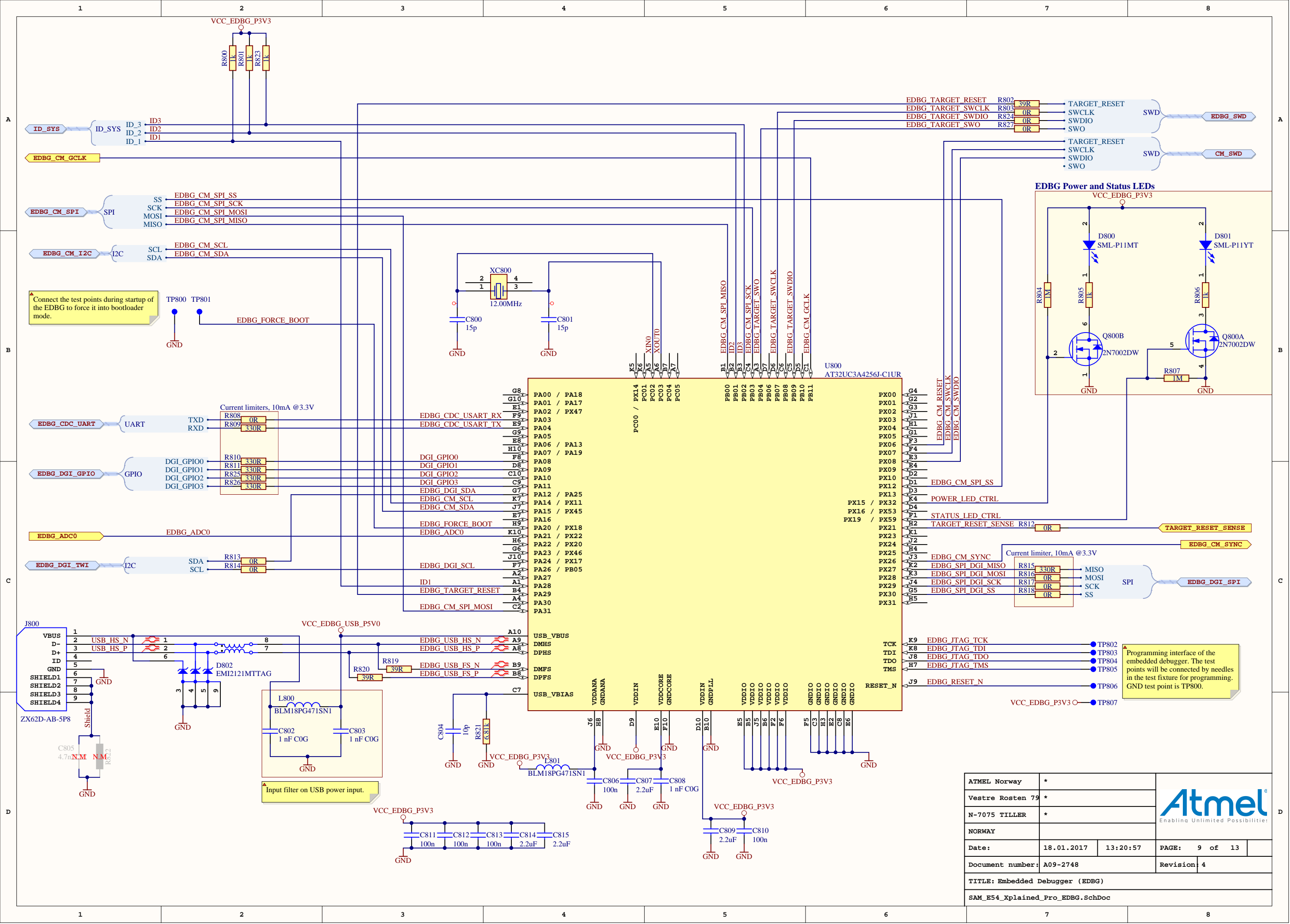


▲ C/D is connected to COM when a card is placed into the socket.  
W/P is connected to COM when the write protect slider is in protect position.

D

D


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NORWAY				
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Document number:	A09-2748		Revision:	4
TITLE: Memory				
SAM_E54_Xplained_Pro_Memory.SchDoc				



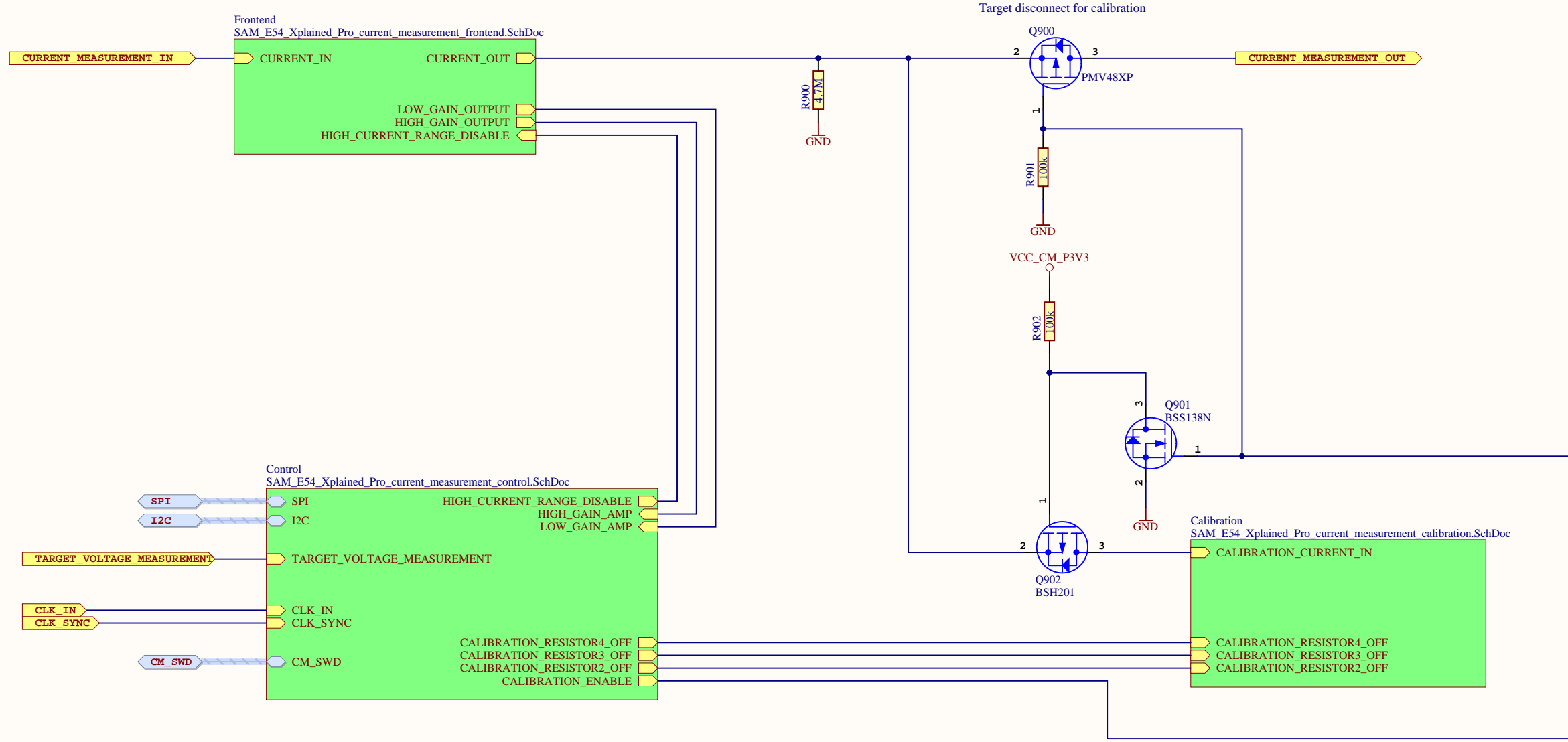
Connect the test points during startup of the EDBG to force it into boot-loader mode.


Input filter on USB power input.

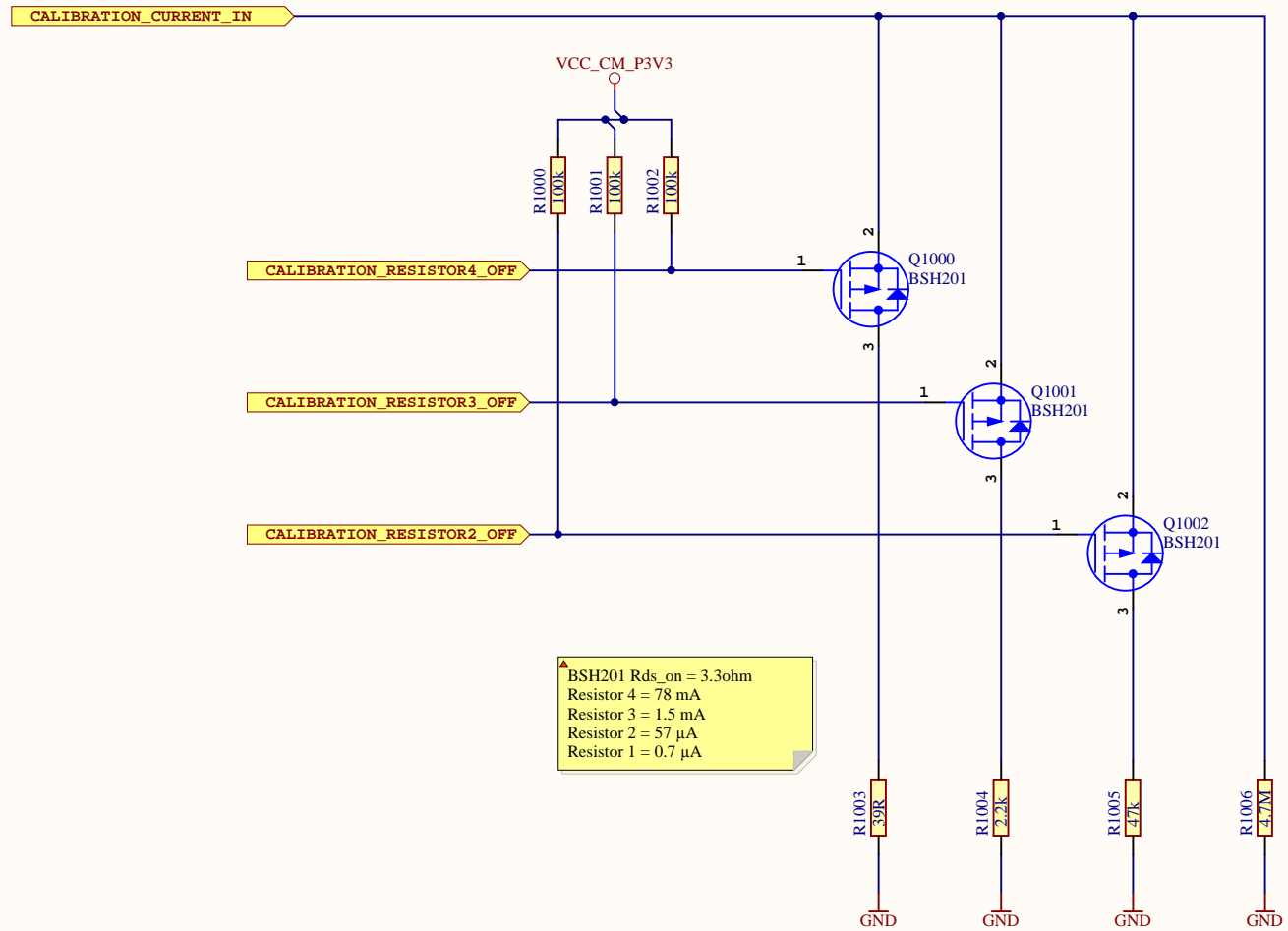
Programming interface of the embedded debugger. The test points will be connected by needles in the test fixture for programming. GND test point is TP800.


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NORWAY				
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Document number:	A09-2748		Revision:	4
TITLE: Embedded Debugger (EDBG)				
SAM_E54_Xplained_Pro_EDBG.SchDoc				

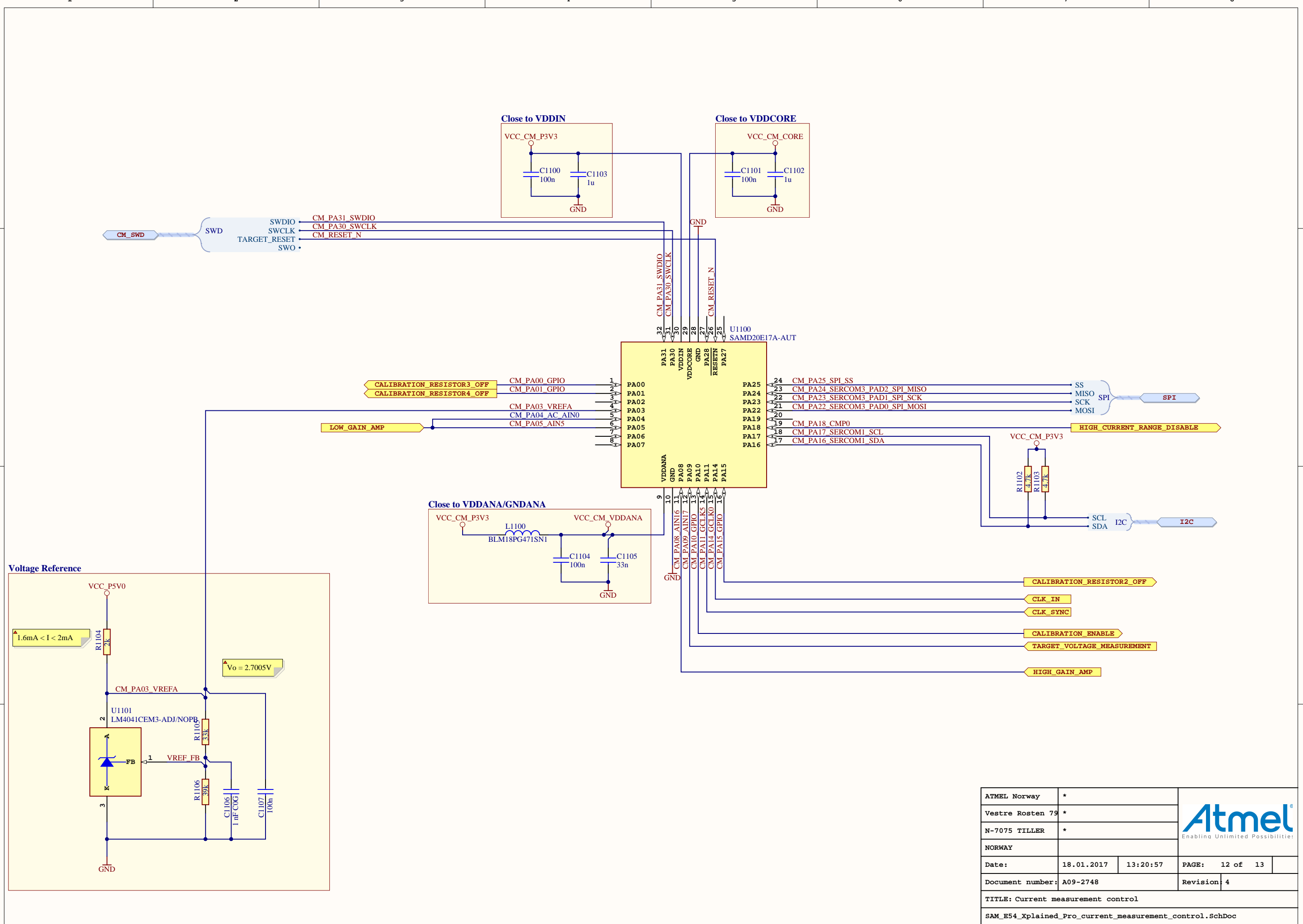




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Vestre Rosten 79	*		
N-7075 TILLER	*		
NORWAY			
Date:	18.01.2017	13:20:57	PAGE: 10 of 13
Document number:	A09-2748		Revision: 4
TITLE: Current measurement			
SAM_E54_Xplained_Pro_current_measurement.SchDoc			

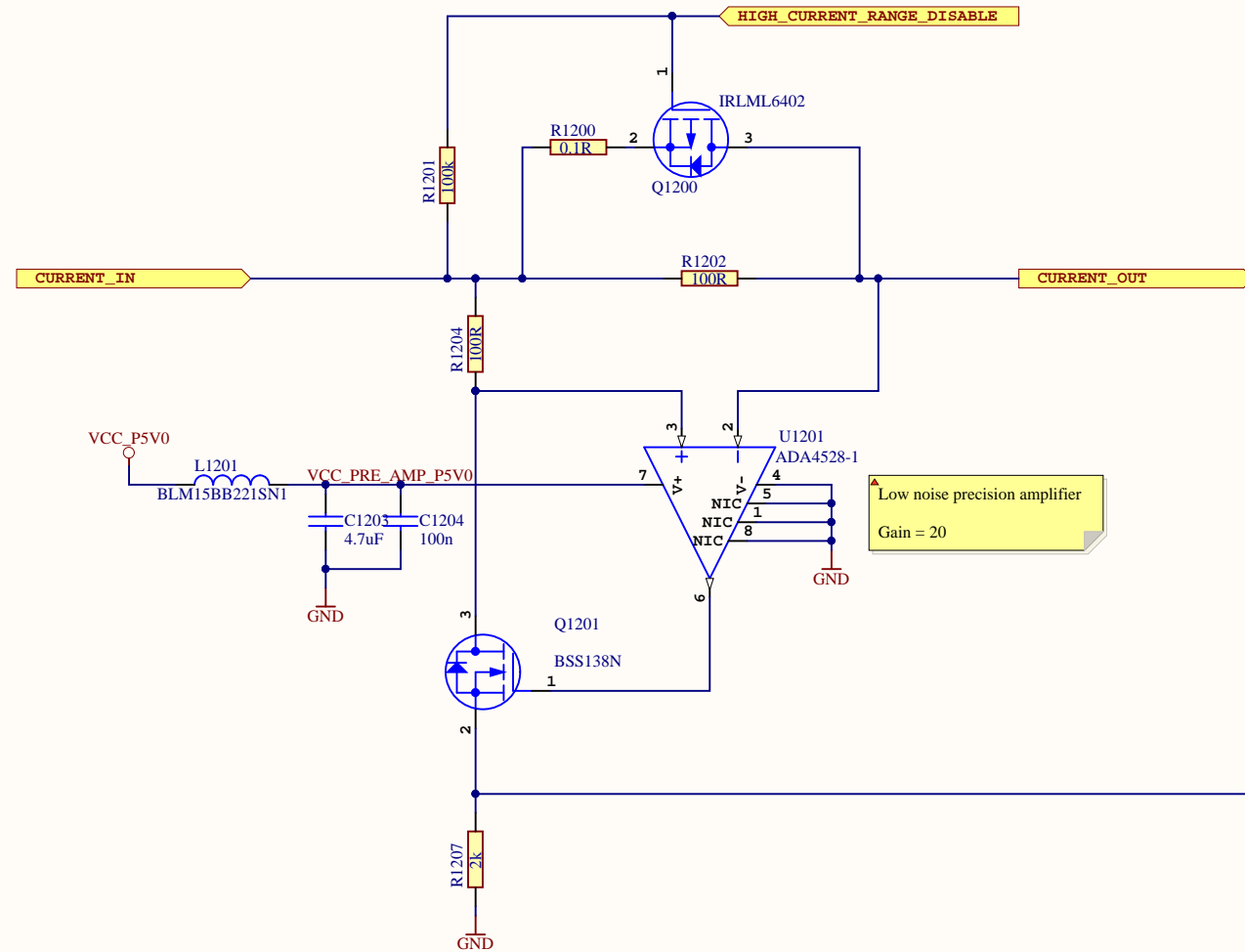


ATMEL Norway	*		
Vestre Rosten 79	*		
N-7075 TILLER	*		
NORWAY			
Date:	18.01.2017	13:20:57	PAGE: 11 of 13
Document number:	A09-2748		Revision: 4
TITLE: Current measurement calibration			
SAM_E54_Xplained_Pro_current_measurement_calibration.SchDoc			



ATMEL Norway	*		
Vestre Rosten 79	*		
N-7075 TILLER	*		
NORWAY			
Date:	18.01.2017	13:20:57	PAGE: 12 of 13
Document number:	A09-2748		Revision: 4
TITLE: Current measurement control			
SAM_E54_Xplained_Pro_current_measurement_control.SchDoc			

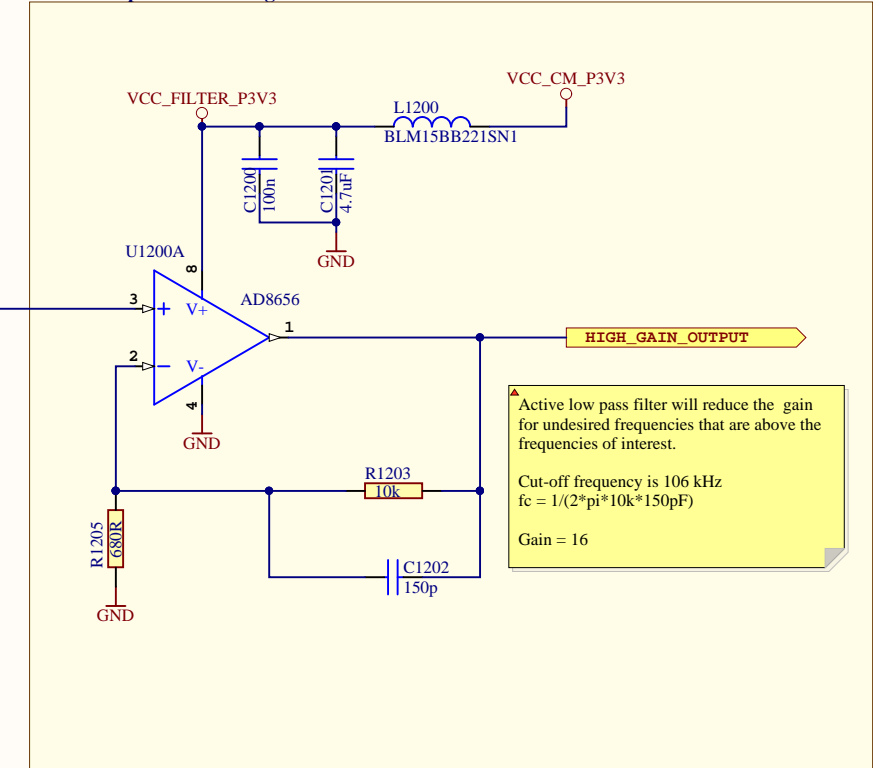




Low noise precision amplifier  
Gain = 20

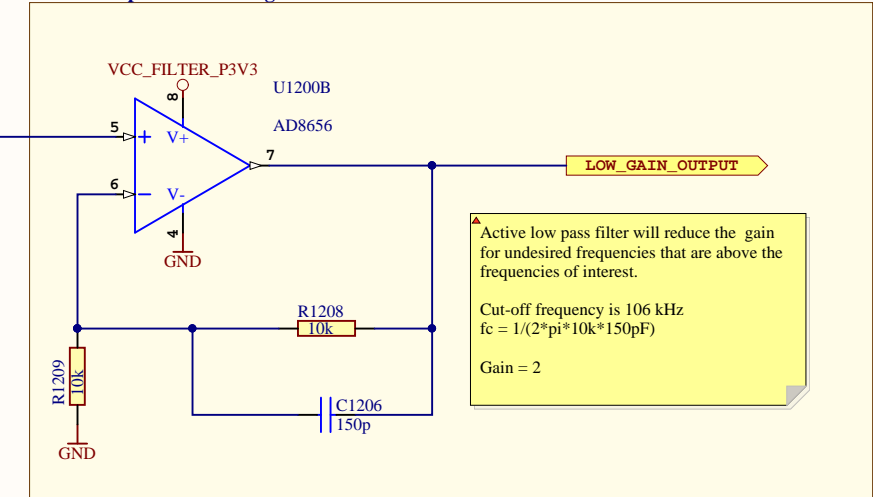
HIGH\_CURRENT\_RANGE\_DISABLE

Active low pass filter with gain




Active low pass filter will reduce the gain for undesired frequencies that are above the frequencies of interest.  
Cut-off frequency is 106 kHz  
 $f_c = 1/(2*\pi*10k*150pF)$   
Gain = 16

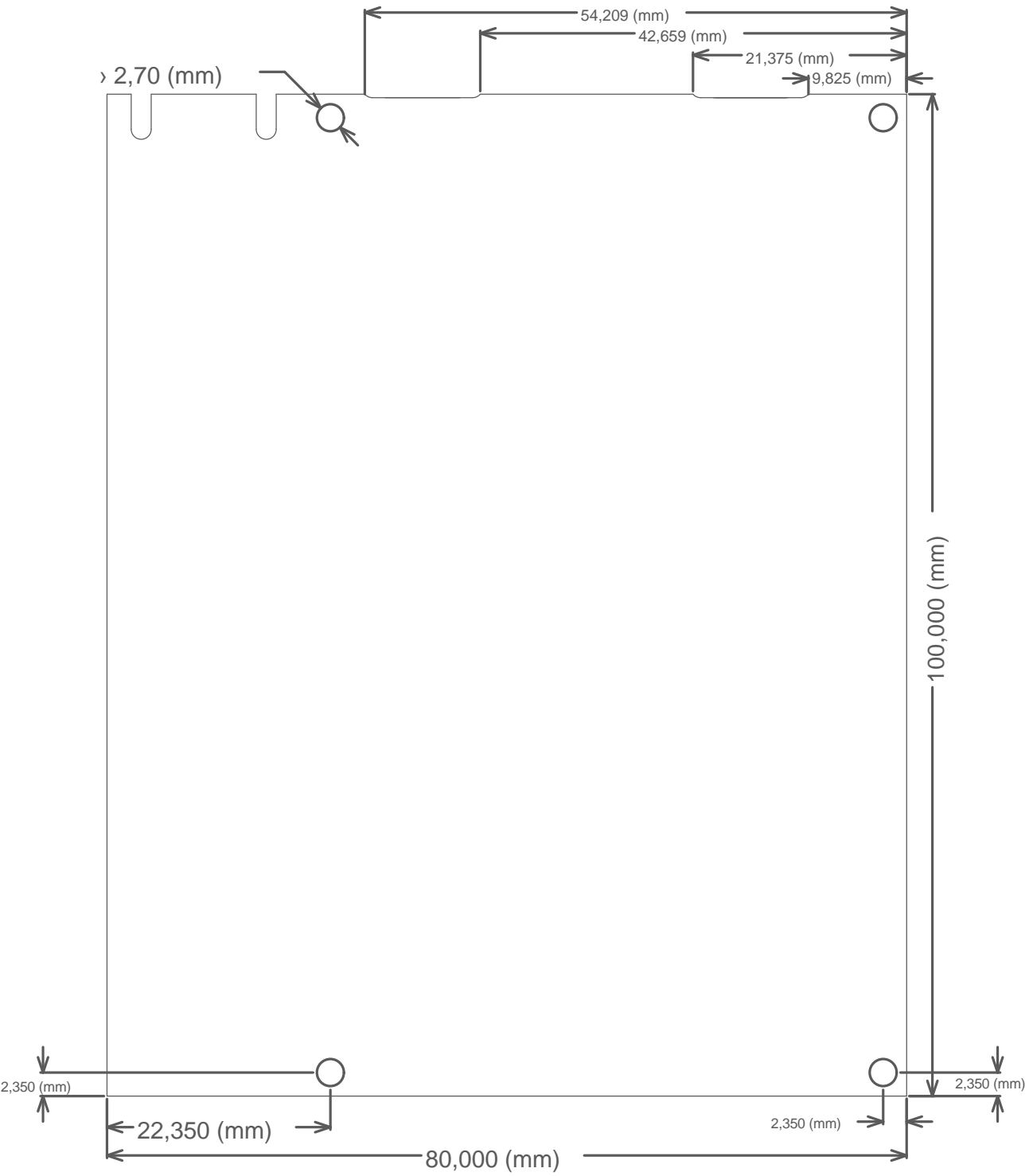
Active low pass filter with gain



Active low pass filter will reduce the gain for undesired frequencies that are above the frequencies of interest.  
Cut-off frequency is 106 kHz  
 $f_c = 1/(2*\pi*10k*150pF)$   
Gain = 2

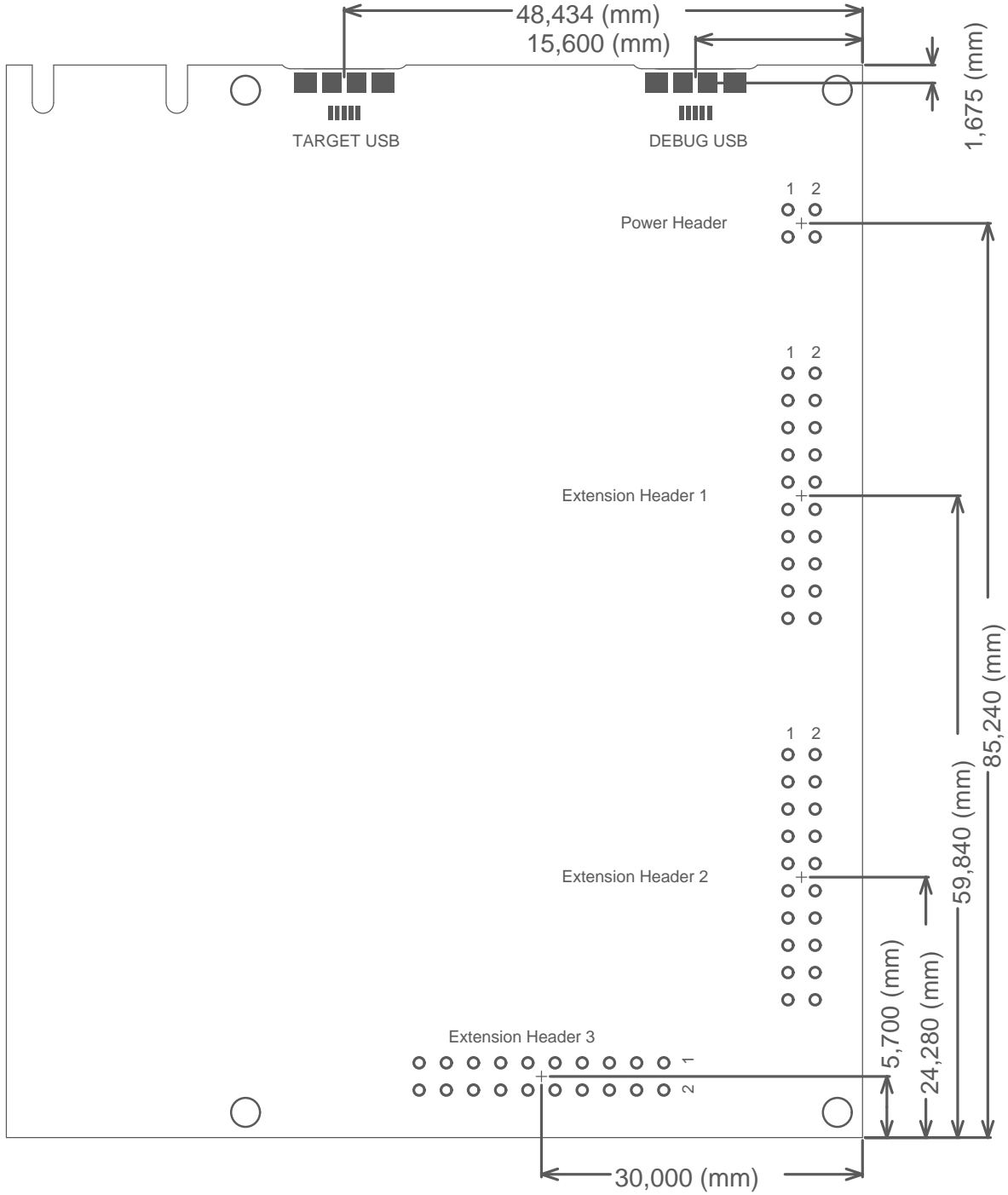
ATMEL Norway	*			 Enabling Unlimited Possibilities
Vestre Rosten 79	*			
N-7075 TILLER	*			
NORWAY				
Date:	18.01.2017	13:20:57	PAGE: 13 of 13	
Document number:	A09-2748		Revision:	4
TITLE: Current measurement frontend				
SAM_E54_Xplained_Pro_current_measurement_frontend.SchDoc				

# Mechanical Dimensions

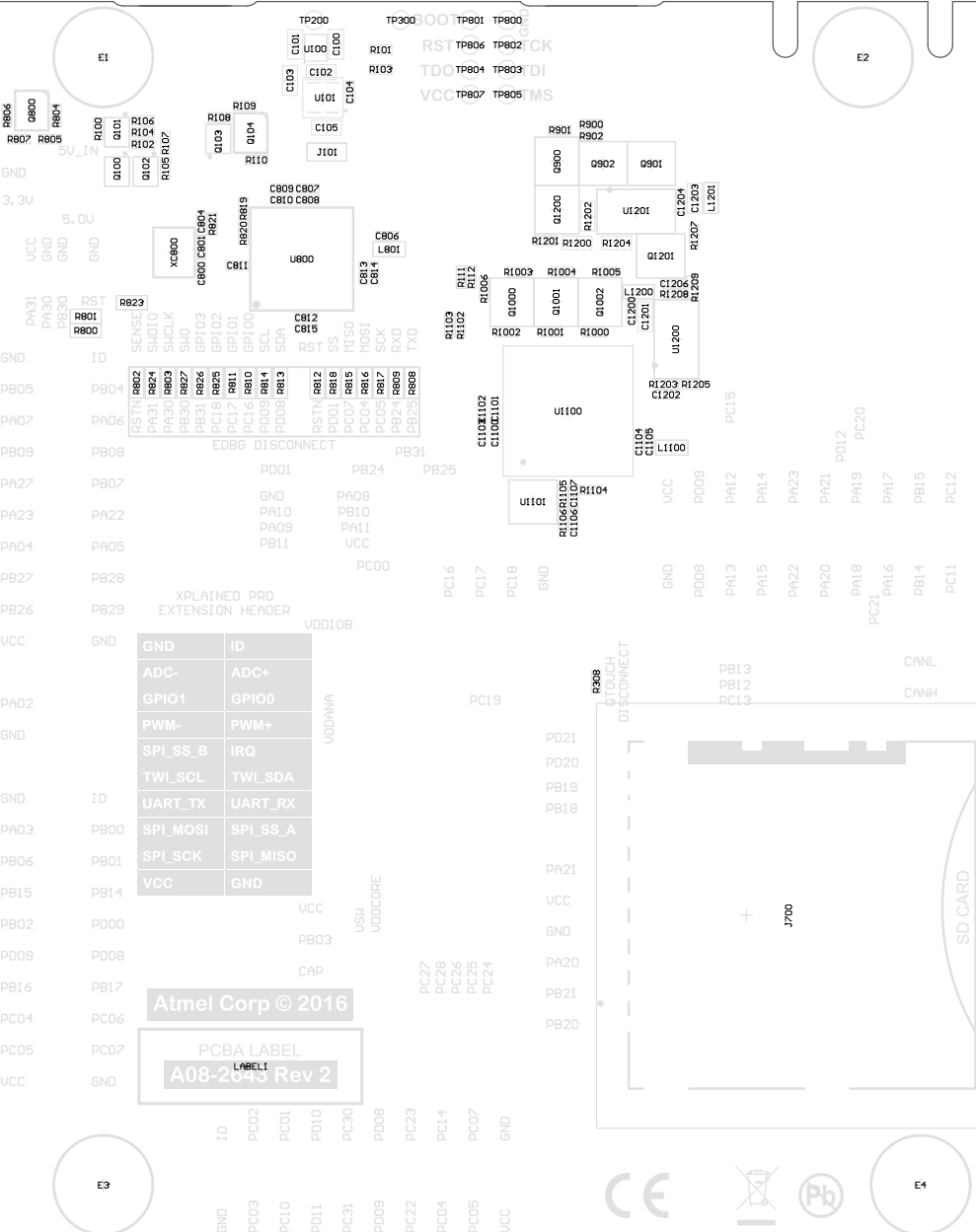




# Connector Placement







GND	ID
ADC-	ADC+
GPIO1	GPIO0
PWM-	PWM+
SPI_SS_B	IRQ
TWI_SCL	TWI_SDA
UART_TX	UART_RX
SPI_MOSI	SPI_SS_A
SPI_SCK	SPI_MISO
VCC	GND

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